

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	((("5294829") or ("4539660") or ("5161304") or ("4872844") or ("5403782")).PN.	US-PGPUB; USPAT	OR	OFF	2005/01/06 06:42
L2	5	1 and batter\$3 and (integrat\$3 adj circuit adj chip\$2) and package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:31
L3	5	1 and batter\$3 with (integrat\$3 adj circuit adj chip\$2) with package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 07:05
L4	37	batter\$3 with (integrat\$3 adj chip\$2) with package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 07:05
L5	5	1 and batter\$3 with (integrat\$3 adj circuit adj chip\$2) and package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 07:05
L6	120	batter\$3 with (integrat\$3 adj circuit adj chip\$2) and package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 07:07
L7	47	batter\$3 with (integrat\$3 adj circuit adj chip\$2) same package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:34
L8	1	(multi adj3 batter\$3) with (integrat\$3 adj circuit adj chip\$2) same package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:02
L9	1	(multi adj3 batter\$3) with (integrat\$3 adj circuit adj chip\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:03
L10	4	((multi or plural\$3) adj3 batter\$3) with (integrat\$3 adj circuit adj chip\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:21

L11	0	((multi or plural\$3) adj3 batter\$3) with stack\$2 with (integrat\$3 adj5 chip\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:07
L12	0	((multi or plural\$3) adj3 batter\$3) with stack\$2 same (integrat\$3 adj5 chip\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:07
L13	0	((multi or plural\$3) adj3 batter\$3) with stack\$2 same chip\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:08
L14	315	((multi or plural\$3) adj3 batter\$3) with stack\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:09
L15	13	14 and (integrat\$3 with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:33
L16	20	batter\$3 with (multi adj chip\$2) same package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 08:43
L17	17	batter\$3 with (multi adj chip\$2) with package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 09:13
L18	14	batter\$3 with (multi adj chip\$2 adj module) with package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 09:20
L19	26	batter\$3 with (multi adj chip\$2 adj module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 09:53
L20	48	batter\$3 same (multi adj chip\$2 adj module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 10:25

L21	8646	(multi adj chip\$2 adj module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 10:25
L22	162	(multi adj chip\$2 adj module) with stack	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 10:26
L23	31	((multi or plural\$3) adj3 batter\$3) with chip\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:23
L24	0	((multi or plural\$3) adj3 batter\$3) with stack with chip\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:23
L25	0	((multi or plural\$3) adj3 batter\$3) with stack same chip\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:24
L26	8	((multi or plural\$3) adj3 batter\$3) with stack and chip\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:28
L27	3838	174/52.4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:31
L28	23	27 and (batter\$3 and (integrat\$3 adj circuit adj chip\$2) and package)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:47
L29	2529	361/395	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:41
L30	16	29 and (batter\$3 and (integrat\$3 adj circuit adj chip\$2) and package)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:53

L31	1287	361/392	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:50
L32	14	31 and (batter\$3 and (integrat\$3 adj circuit adj chip\$2) and package)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/06 11:57